

Fig. 1

BASIC IMPLEMENTATION OF THE INVENTION

100 FFT

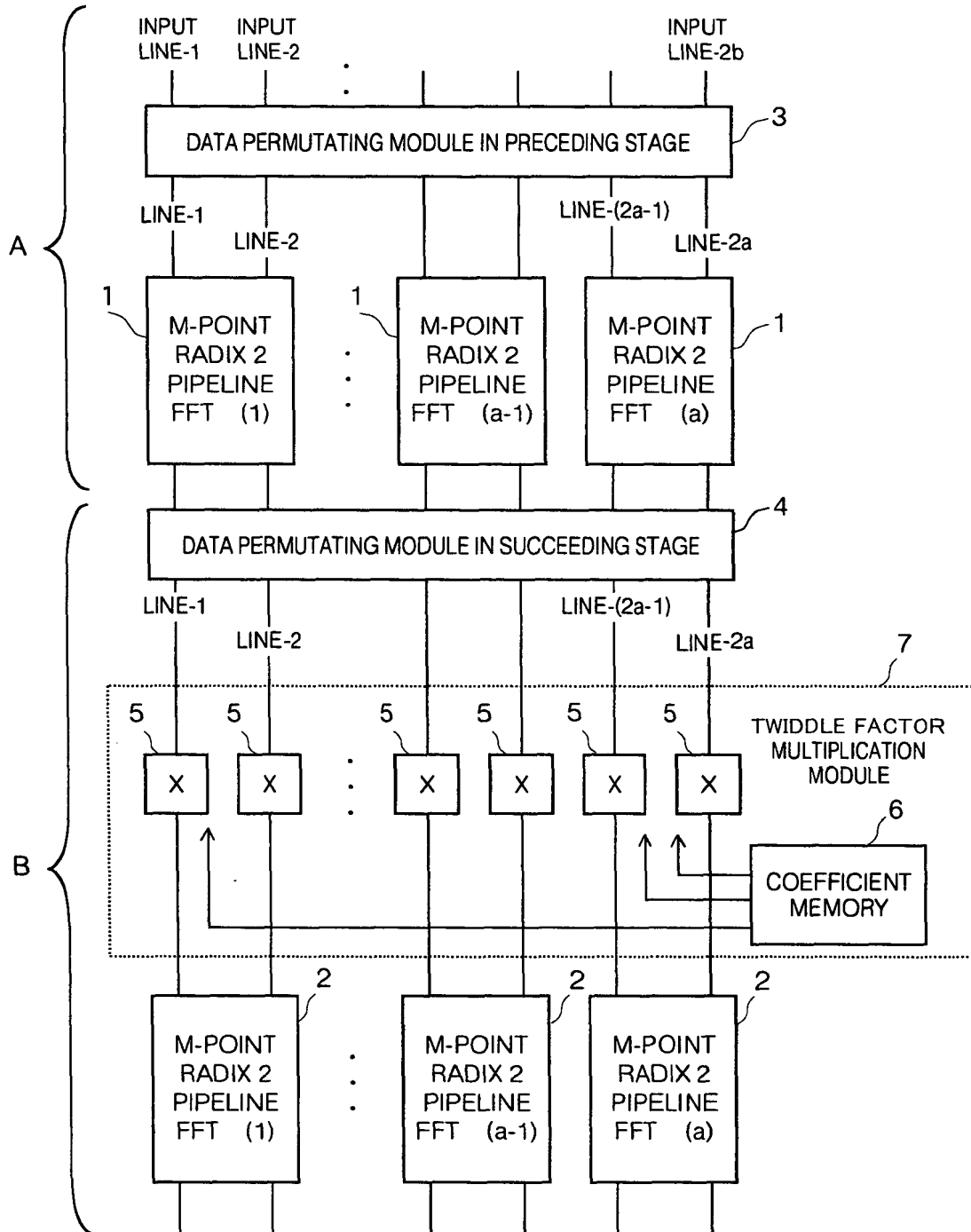


Fig. 2

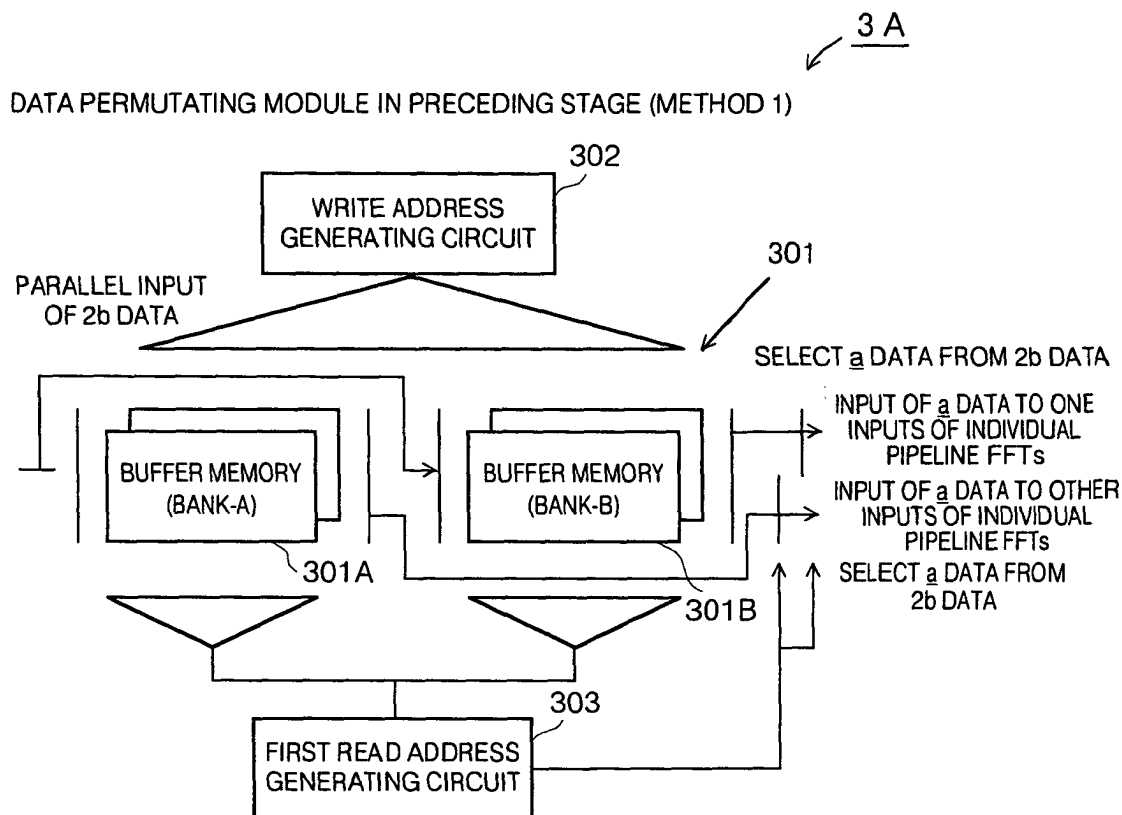


Fig. 3

FIRST READ ADDRESS GENERATING CIRCUIT

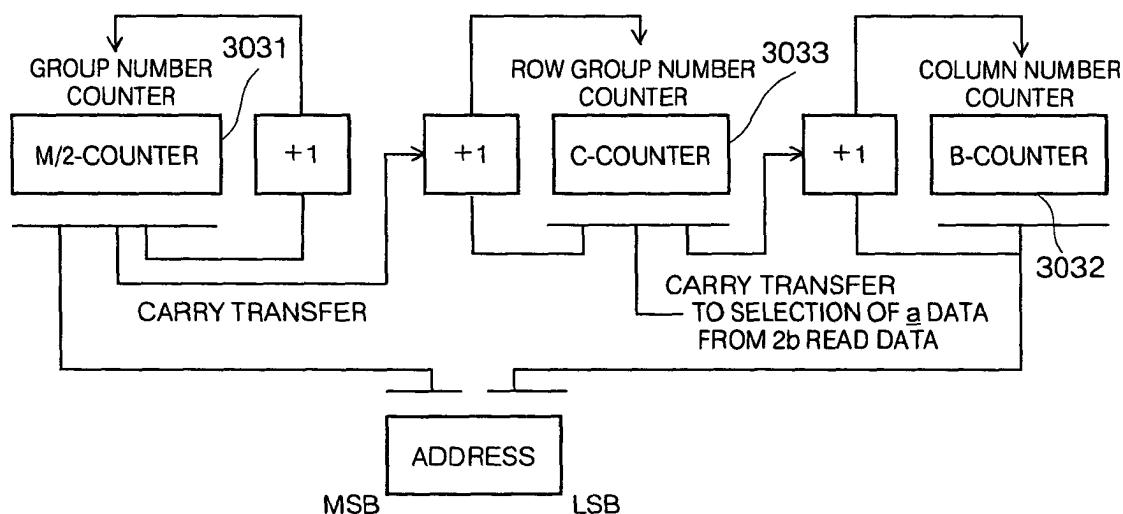


Fig. 4

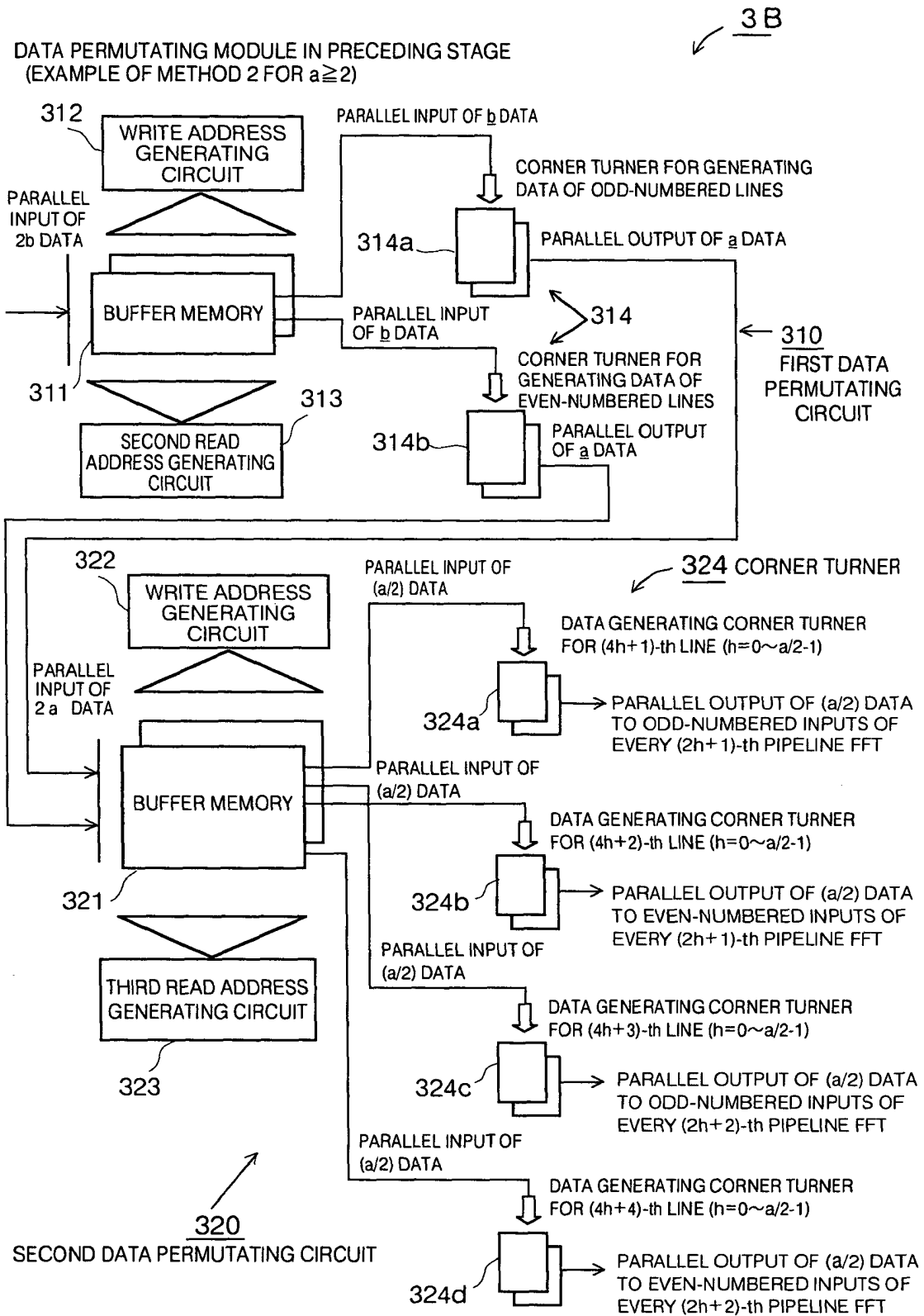


Fig. 5

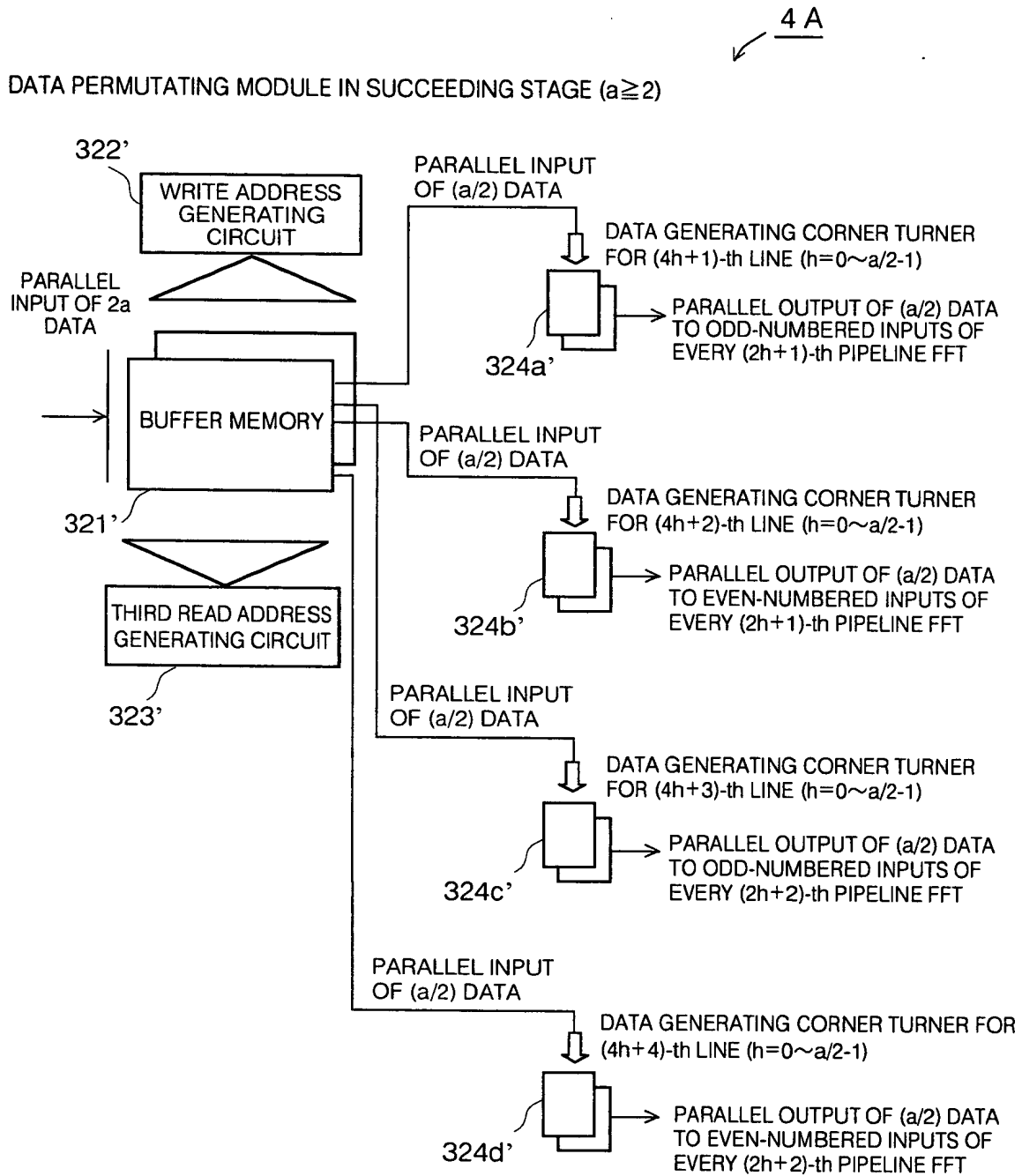


Fig. 6

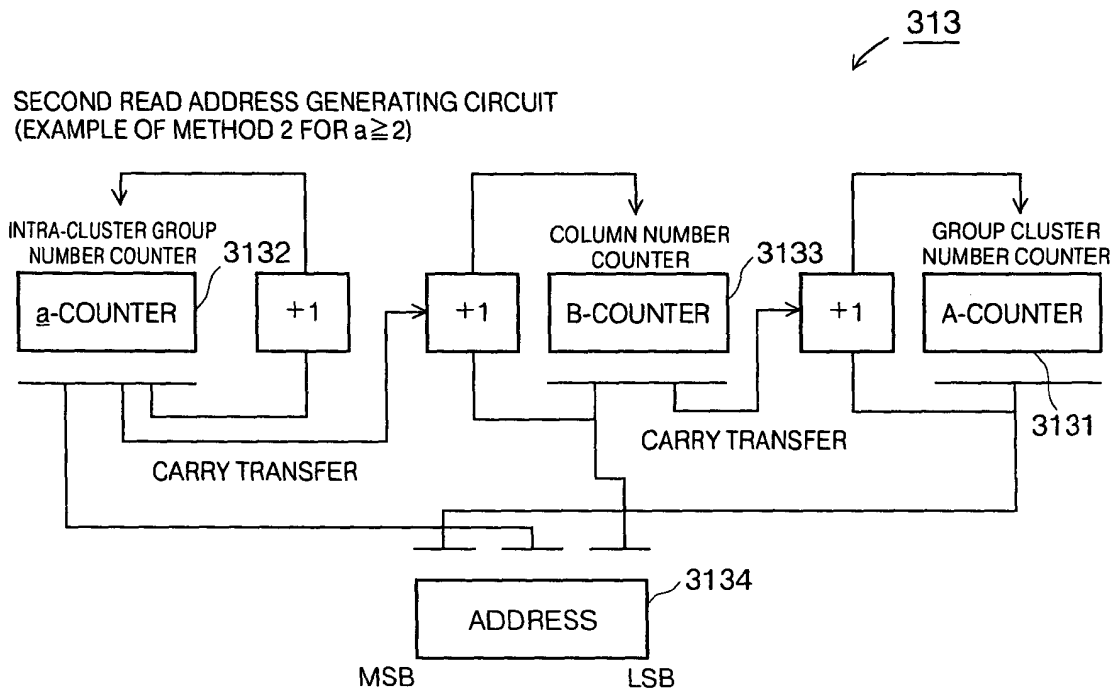


Fig. 7

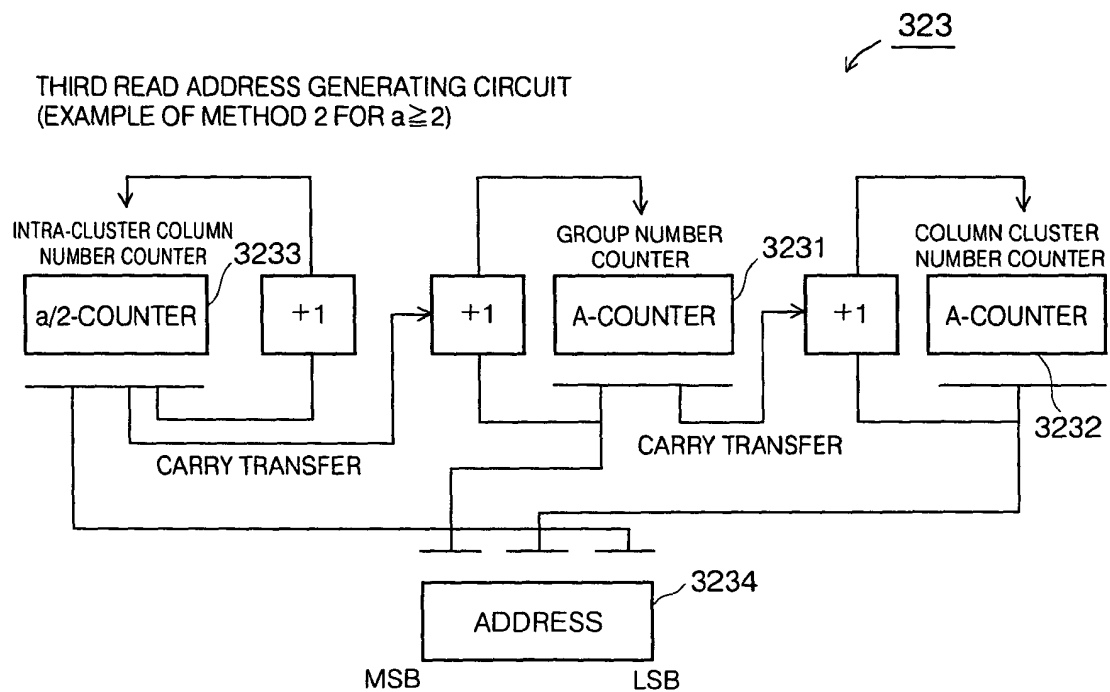


Fig. 8

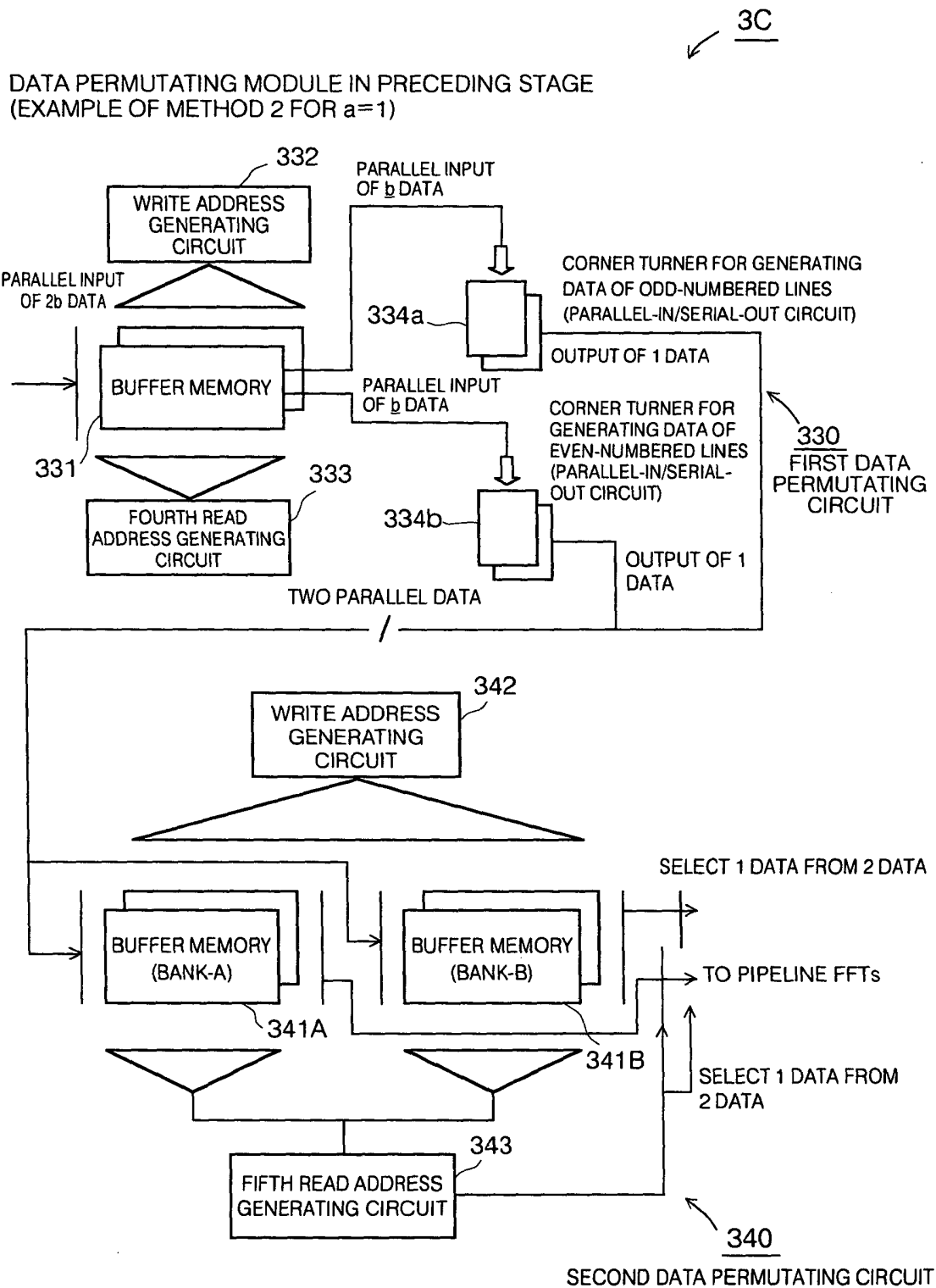


Fig. 9

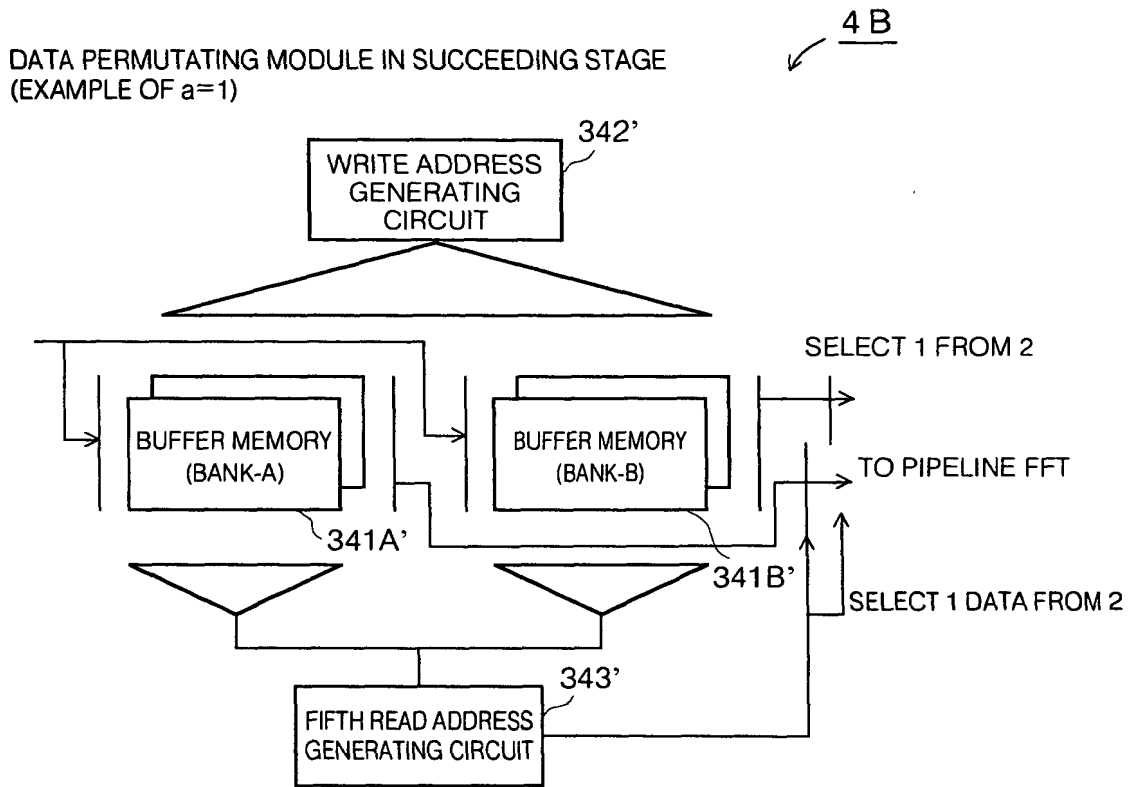


Fig.10

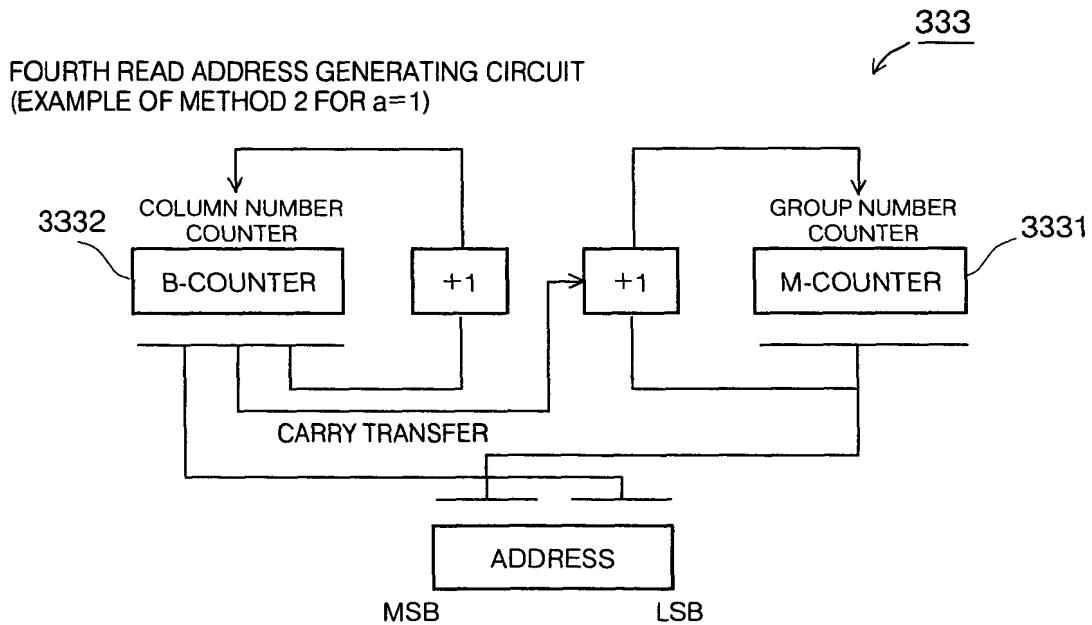


Fig.11

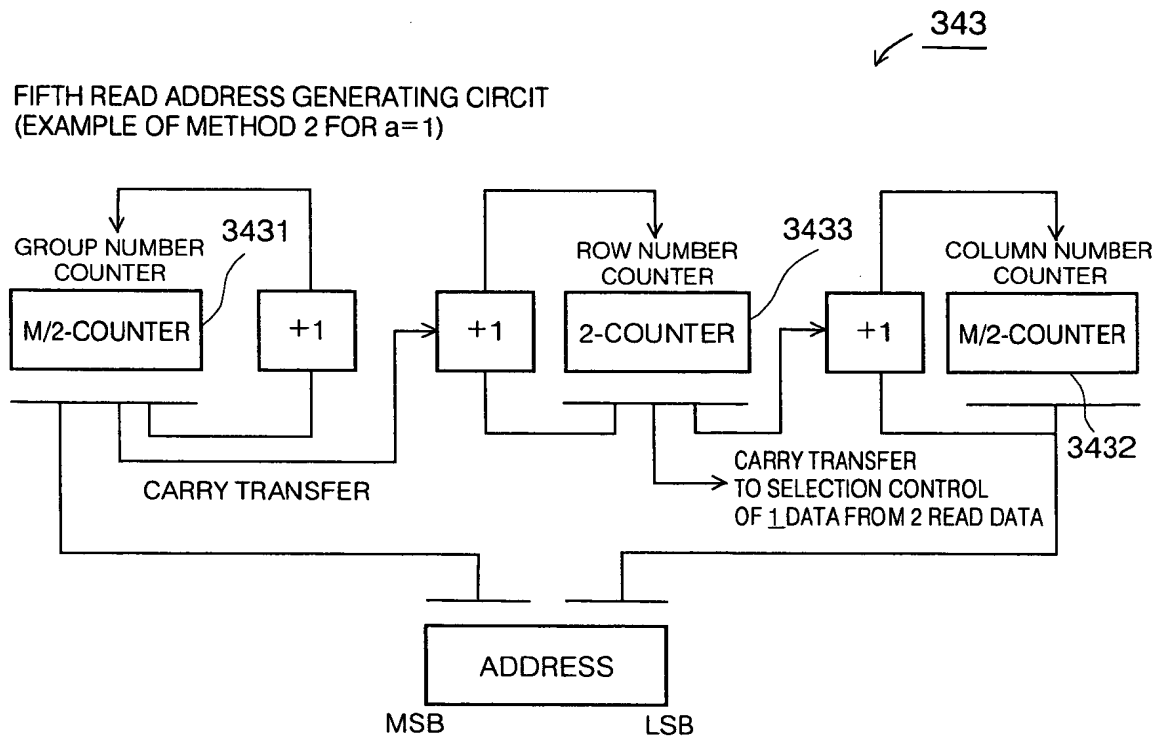


Fig.12

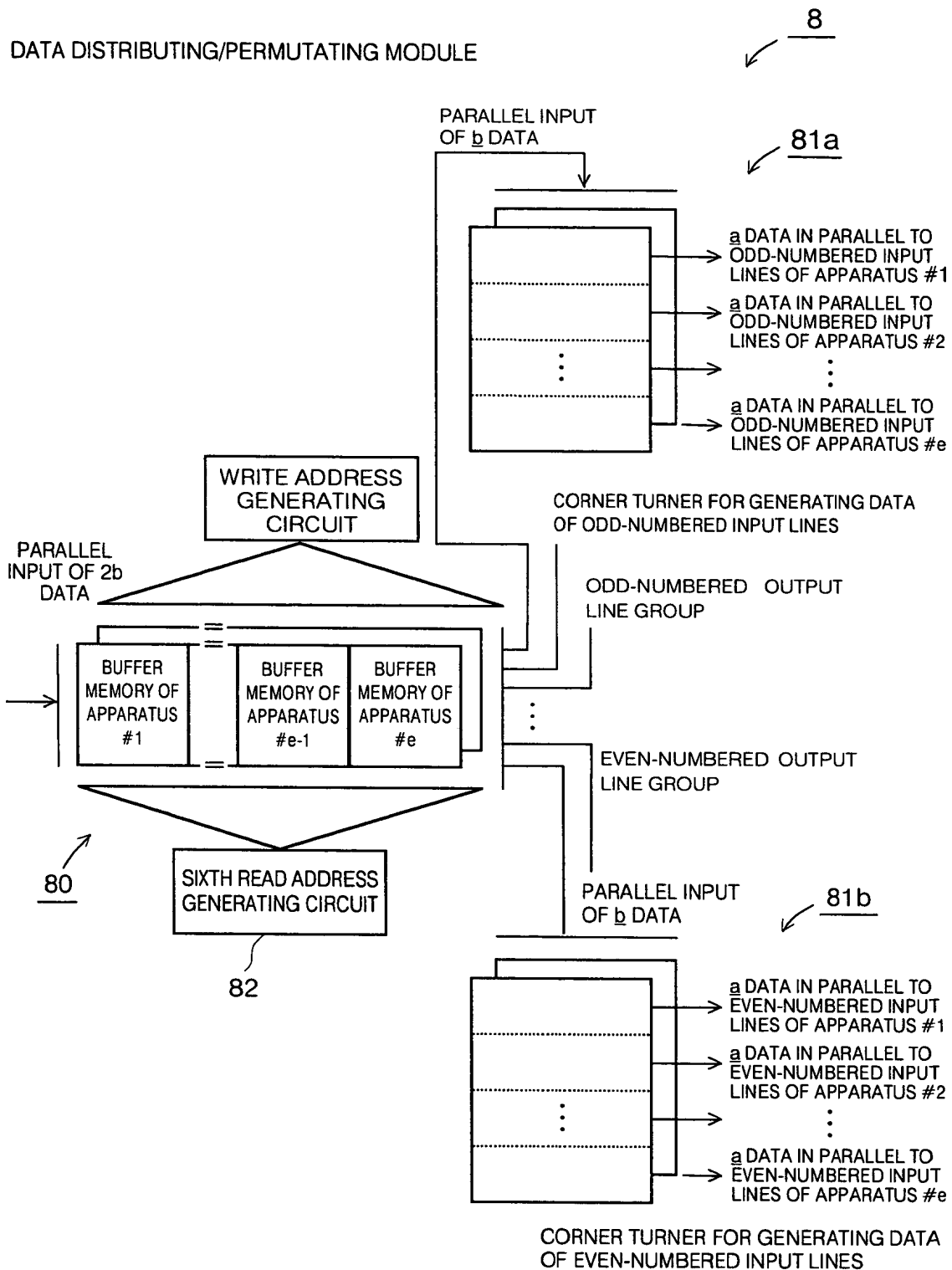


Fig.13

SIXTH READ ADDRESS GENERATING CIRCUIT (EXAMPLE FOR $a \geq 2$)

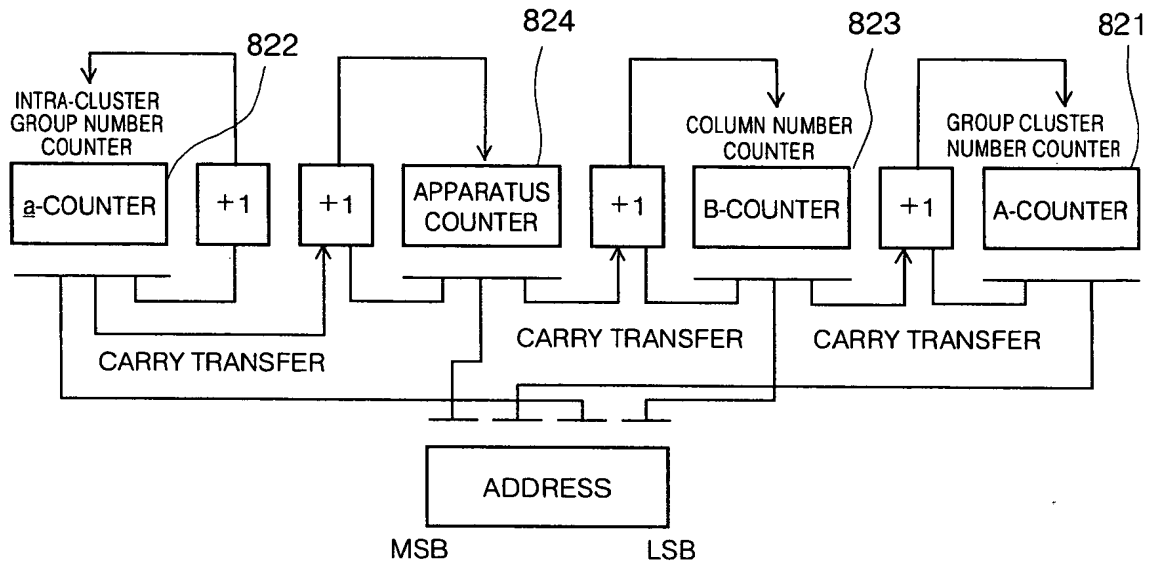


Fig.14

SIXTH READ ADDRESS GENERATING CIRCUIT (EXAMPLE OF $a=1$)

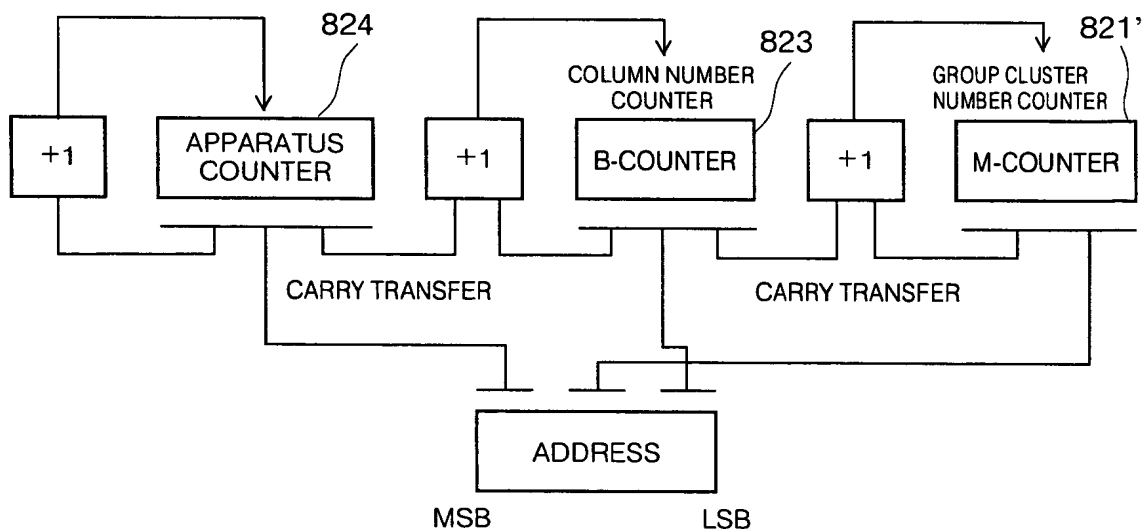


Fig.15

64-POINT FFT

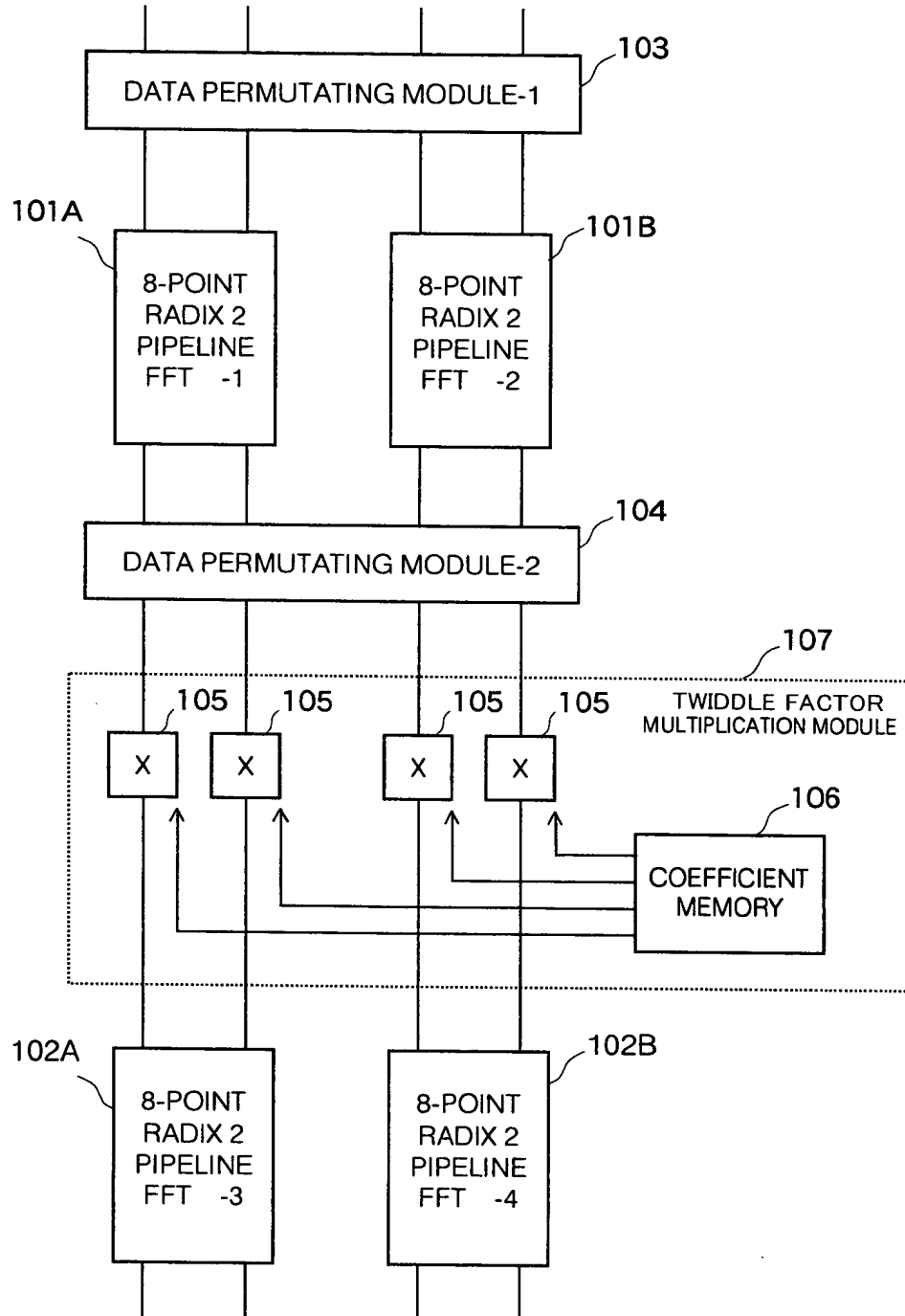


Fig.16

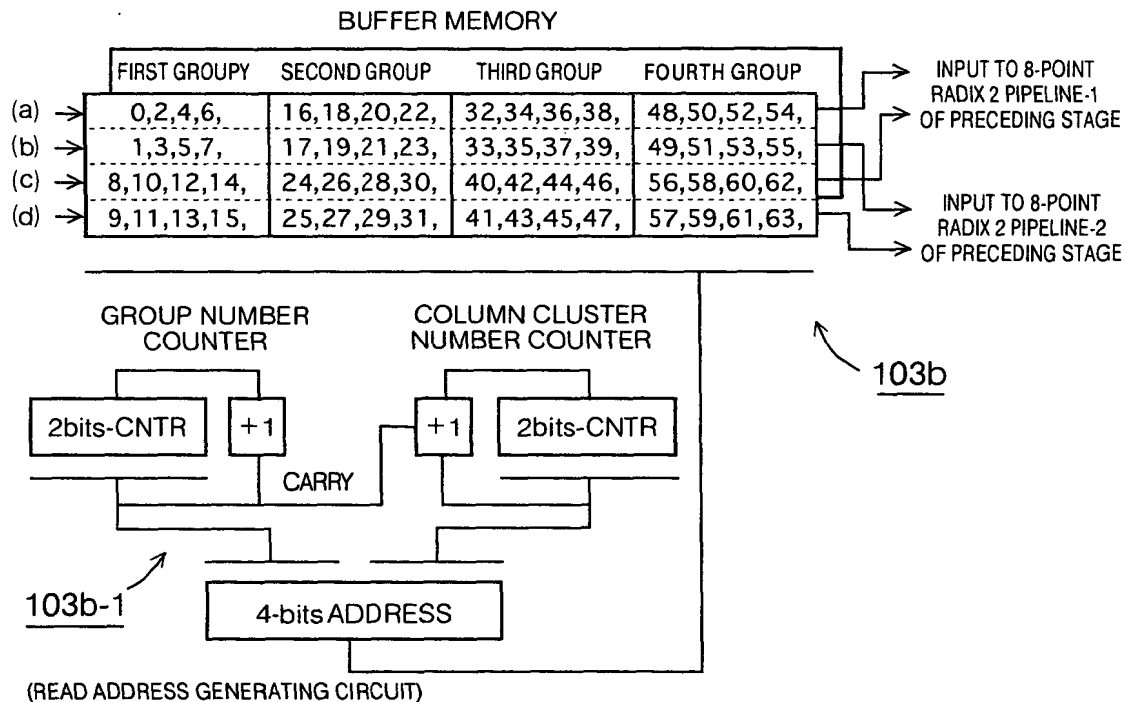
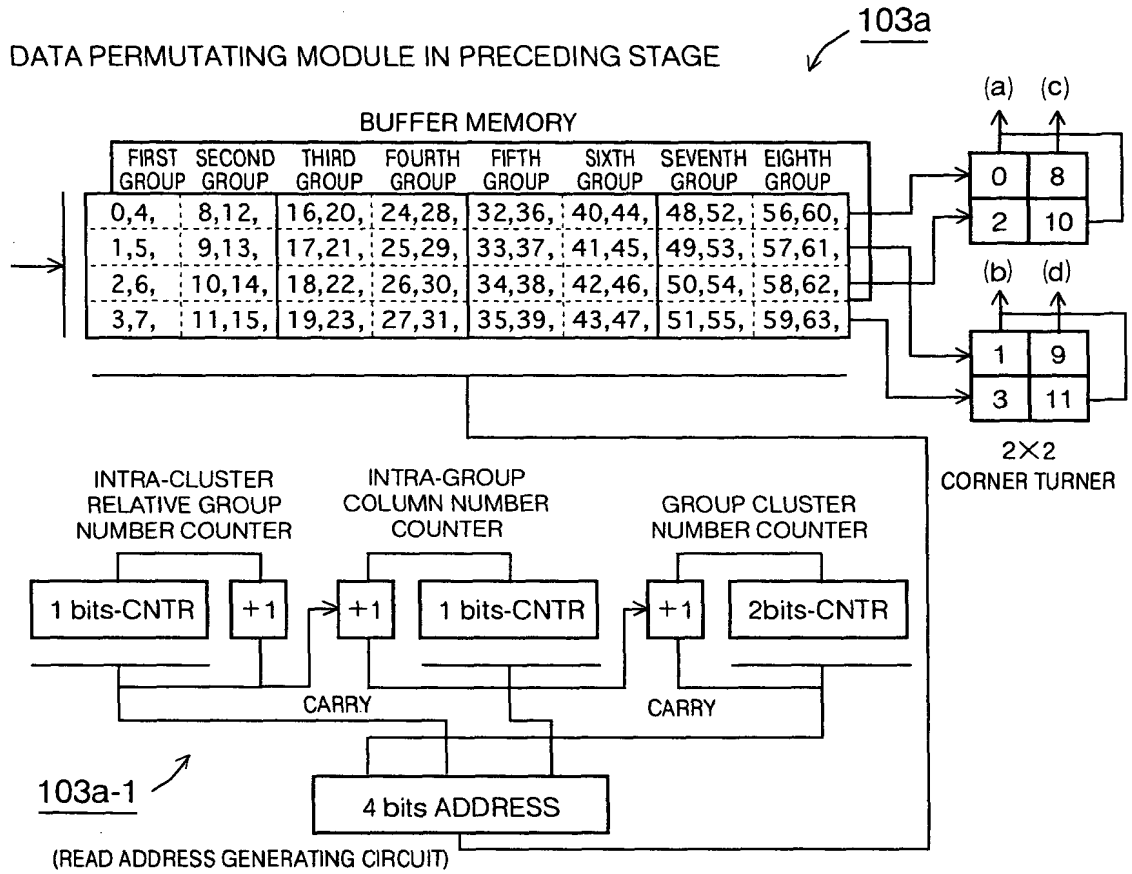


Fig.17

